

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tokuroh OZAWA et al.

Group Art Unit:

Application No.: New Rule 53(b) Continuation of  
U.S. Application No. 10/267,834

Examiner: Not Yet Assigned

Filed: July 24, 2003

Docket No.: 100629.03

For: DISPLAY APPARATUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The references were cited by or submitted to the Office in parent application No. 10/267,834, filed October 10, 2002, which is relied upon for an earlier filing date under 35 U.S.C. §120. Thus, copies of these references are not attached. 37 CFR §1.98(d).

Respectfully submitted,



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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 100629.03		RULE 53(b) DIVISION OF APPLICATION NO. 10/267,834	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANT(S) Takuroh OZAWA et al.			
FILING DATE July 23, 2003				GROUP			

  

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		5,714,838	2-1998	HAIGHT et al.		
		6,072,450	6-2000	YAMADA et al.		
		5,587,329	12-96	HSEUH et al.		
		5,828,429	10-98	TAKEMURA		
		6,023,307	02-00	TAKEMURA		
		5,670,792	09-97	UTSUGI et al.		
		6,011,529	01-00	IKEDA		
		5,995,073	11-99	ISAMI et al.		
		6,243,069	06-01	OGAWA et al.		
		5,869,929	02-99	EIDA et al.		

  

FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
		EP-A1-0 653 741	05-95	Europe		
		JP-A-8-129358	05-96	Japan		
		JP 8-227276	09-96	Japan		
		JP 08 241057 A	09-96	Japan		
		EP 0112 700 A	07-84	Japan		
		WO 96 06456 A	02-96	WIPO		
		JP 08 129360 A	05-96	Japan		
		EP 0717 439 A	06-96	Europe		
		JP 08-241057 (translation only)	9-96	Japan		

  

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
	Alan G. Lewis, et al., <i>Physical Mechanisms For Short Channel Effects In Polysilicon Thin Film Transistors</i> , IEEE, 1989, pgs. 13.4.1-13.4.4  Alan G. Lewis et al., <i>Polysilicon TFT Circuit Design and Performance</i> , IEEE Journal of Solid-State Circuits, December 1992, Vol. 27, No. 12, pgs. 1833-1842.
EXAMINER	DATE CONSIDERED
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Date: July 24, 2003